

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application Of: Kovacic et al. File No: 115-01 US DIV(2)
Application No.: 09/988,938 Group Art: 2823
Filing Date: February 1, 2002 Examiner: Pham, Thanh V
Title: SI-GE Base Heterojunction Bipolar Device

December 10, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Information Disclosure Statement
- Under 37 CFR 1.97(e)(1)

Sir:

Applicant submits herewith an Information Disclosure Statement (Form PTO-1449)
and copies of references listed therein.

The documents listed in the IDS were cited in a Search Report mailed November 28,
2003 from the European Patent Office in the corresponding European application not more
than three months prior to the filing of this statement.

Respectfully submitted,



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Substitute form for form 1449A & 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
		Application No.	09/988,938
		Filing Date	February 01, 2002
		First Named Inventor	Kovacic
		Group Art Unit	2823
		Examiner Name	Pham, Thanh V.
Sheet 1 of 1	Attorney Docket No.	115-01 US DIV(2)	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		5,518,710		Boyd et al.	05-14-1996	

FOREIGN PATENT DOCUMENTS								
Examiner Initials ^a	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T ^b
		Office ³	Number ⁴	Kind Code ⁵ (if known)				

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (In capital letters), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T*
		Sato, F. et al., "Sub-20 psec ECL Circuits with 50 GHz fmax Self-Aligned SiGe HBTs", Electron Devices Meeting, 1992, Technical Digest, International San Francisco, CA, USA, December 13-16, 1992, New York, NY, USA, IEEE, pps 15.2.1-15.2.4	

Examiner Signature	Date Considered
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1 Unique citation designation number. 2 See attached Kinds of U.S. Patent Documents. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

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DATE:	December 10, 2003	NO. OF PAGES INCL THIS PAGE:	7
TO:	United States Patent Office	FAX NO.:	703-872-9318
ATTN:	Examiner Thanh Pham	GROUP ART UNIT:	TC 2823
FROM:	Gordon Freedman	FAX NO.:	(613) 274-7414
RE:	Information Disclosure Statement U.S. Patent Application No. 09/988,938 Filed: February 01, 2002 Inventors: Kovacic et al. Our File: 115-01 US DIV(2)		

**CERTIFICATION OF FACSIMILE TRANSMISSION
UNDER 37 CFR 1.8**

I hereby certify that an Information Disclosure Statement, cover letter and attached reference attached hereto is being transmitted to the United States Patent and Trademark Office, TC 2800, on the date shown below.


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